Abstracts

3-D FDTD design simulation and experimental measurement of a Ka-band planar antipodal linearly-tapered slot antenna (ALTSA)

Liang-Chen Kuo, Meng-Chung Tsai and Huey-Ru Chuang. "3-D FDTD design simulation and experimental measurement of a Ka-band planar antipodal linearly-tapered slot antenna (ALTSA)." 2001 Microwave and Wireless Components Letters 11.9 (Sep. 2001 [MWCL]): 382-384.

Three-dimensional (3-D) finite-difference time-domain (FDTD) simulation of a planar broadband Ka-band (25-35 GHz) antipodal linearly-tapered slot antenna (ALTSA) and the experimental measurements are presented. The Berenger perfectly matched layer (PML) absorbing boundary condition is used for the FDTD computation. A printed ALTSA has been realized by using the RT/Duroid PCB substrate. Good agreement between simulation and measurement were achieved.

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